

FIG. 1 (PRIOR ART)

memory address :

X000: 0000

X000: 0200

X000: 1A00

X000: 2C00

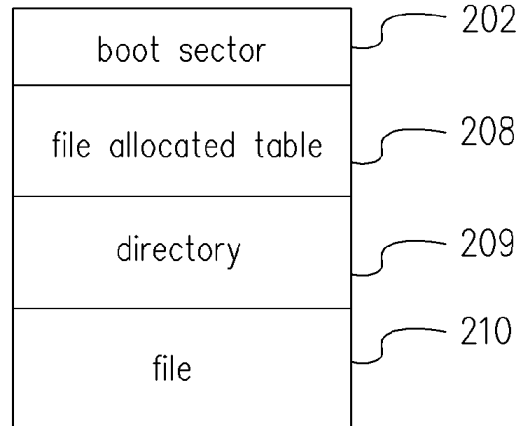


FIG. 2 (PRIOR ART)

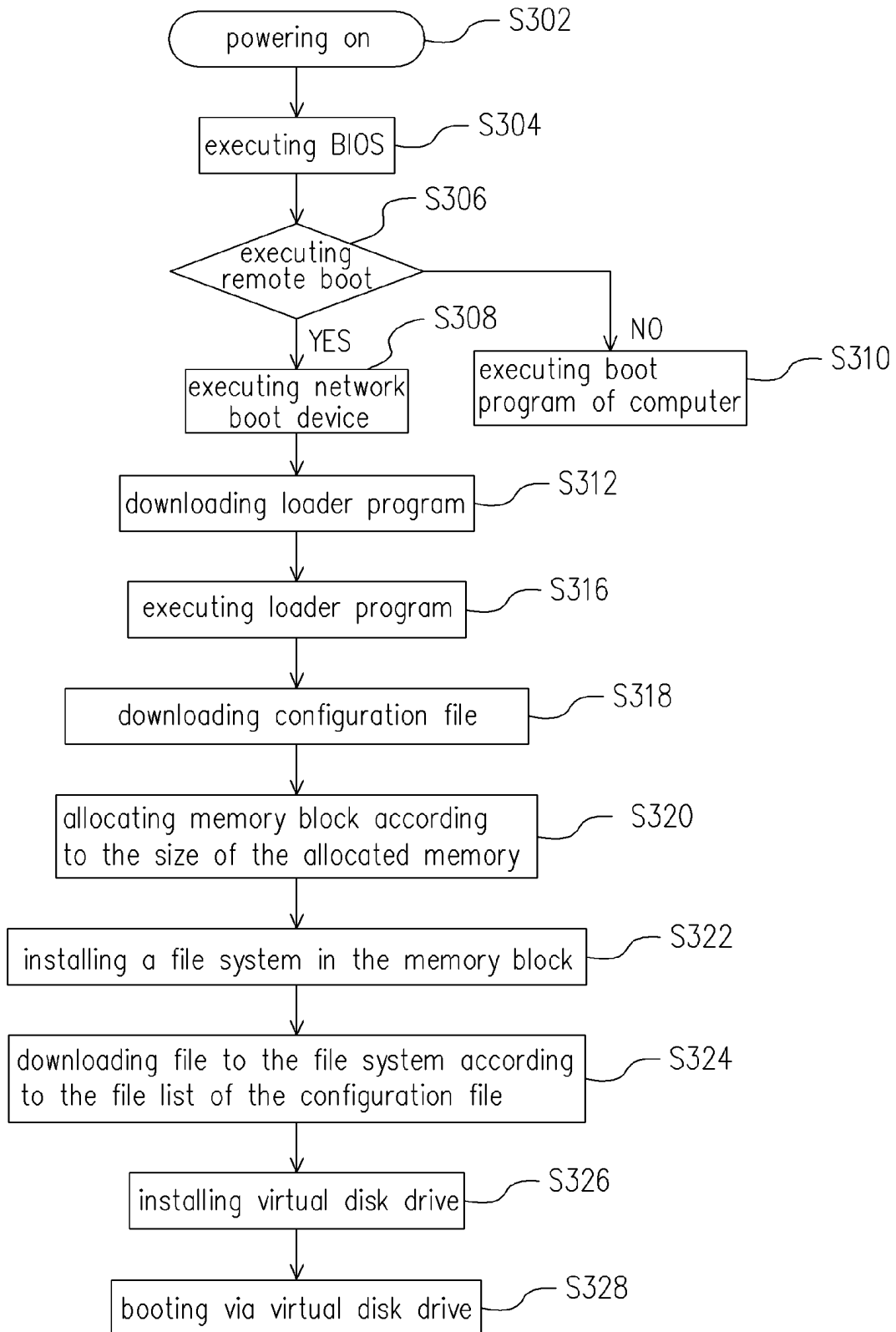


FIG. 3

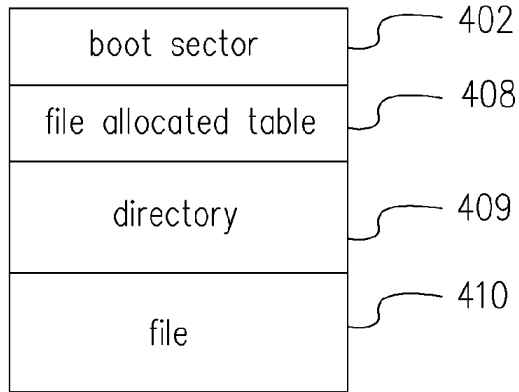


FIG. 4